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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/538,624	02/09/2006	Saleh Osman	PHUS020555	8882
65913	7590	08/14/2007	EXAMINER	
NXP, B.V.			NGUYEN, HIEU P	
NXP INTELLECTUAL PROPERTY DEPARTMENT			ART UNIT	PAPER NUMBER
M/S41-SJ			2817	
1109 MCKAY DRIVE				
SAN JOSE, CA 95131				
			NOTIFICATION DATE	DELIVERY MODE
			08/14/2007	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

Office Action Summary	Application No.	Applicant(s)
	10/538,624	OSMAN ET AL.
	Examiner	Art Unit
	Hieu P. Nguyen	2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-18 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-18 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date: _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date: _____	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

Specification

Applicant is required to update the status (pending, allowed, etc.) of all parent priority applications in the first line of the specification. The status of all citations of US filed applications in the specification should also be updated where appropriate.

The specification has not been checked to the extent necessary to determine the presence to all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Drawings

Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-18 are rejected under 35 U.S.C. 102(b) as being anticipated by Grebennikov et al. (U.S. 6,791,407).

Regarding claim 1 and 14-15, Grebennikov discloses in Fig. 1 and related text an amplifier circuit/method comprising: a driver stage (102) having first active devices (118/120) which receive a signal (disposed around 104) for pre-amplification and output a pre-amplified signal (disposed around 130); an output stage (106) having second active devices (136/138) which receive said pre-amplified signal for further amplification and output an amplified signal (disposed around 144); a detector (not shown) which measures levels of forward signal and reflected signal of said amplified signal, since Grebennikov broadly mentions in col. 6 that the information signal 147, for instance, may be the present output level from the output 108 of the amplifier 110; and a control circuit (110) which controls turning on and off of said first active devices and said second active devices as a function of said levels of forward signal and reflected signal to substantially maintain linearity of said amplifier circuit with load variations, **meeting claims 1 and 14-15.**

Regarding claim 2, the circuit of Grebennikov (see Fig. 1) can be read as the amplifier circuit of claim 1, wherein said output stage is coupled to a load without an isolation device between said output stage and said load, **meeting claim 2.**

Regarding claims 3 and 16, Grebennikov further discloses the amplifier circuit of claim 1, wherein said control circuit independently controls each of said first active devices and said second active devices as mentioned e.g. in col. 6, **meeting claims 3 and 16.**

Regarding claims 4 and 17, Grebennikov further discloses the amplifier circuit of claim 1, wherein said control circuit independently controls each of said first active devices as mentioned in col. 3, **meeting claims 4 and 17.**

Regarding claims 5 and 18, Grebennikov further discloses the amplifier circuit of claim 1, wherein said control circuit independently controls each of said second active devices as mentioned in col. 4, **meeting claims 5 and 18.**

Regarding claim 6, Grebennikov discloses the amplifier circuit of claim 1, wherein said first active devices and said second active devices are NPN transistors as mentioned in col. 3, **meeting claim 6.**

Regarding claim 7, Grebennikov discloses the amplifier circuit of claim 1, further comprising an input match circuit (112) coupled between an input of said amplifier circuit and said driver stage for matching an input impedance of said amplifier circuit to an output impedance of a device coupled to said input, **meeting claim 7.**

Regarding claim 8, Grebennikov discloses the amplifier circuit of claim 7, further comprising at least one capacitor coupled between said input match circuit and said driver stage,

since the matching circuits include capacitors and inductors as mentioned in col. 4, **meeting claim 8.**

Regarding claim 9, similar to claim 8, Grebennikov discloses the amplifier circuit of claim 1, further comprising at least one capacitor coupled between an input of said amplifier circuit and said driver stage as mentioned in col. 4, **meeting claim 9.**

Regarding claim 10, Grebennikov discloses the amplifier circuit of claim 1, further comprising an inter-stage match circuit (128) coupled between an output of said driver stage and an input of said output stage for matching an input impedance of said output stage to an output impedance of said driver stage, **meeting claim 10.**

Regarding claim 11, Grebennikov discloses the amplifier circuit of claim 10, further comprising at least one capacitor coupled between said inter-stage match circuit and said output stage, since the inter-stage matching circuit 128 may includes capacitors and inductors as mentioned in col. 4, **meeting claim 11.**

Regarding claim 12, similar to claim 11, Grebennikov discloses the amplifier circuit of claim 1, further comprising at least one capacitor coupled between said inter-stage match circuit and said output stage as mentioned in col. 4, **meeting claim 12.**

Regarding claim 13, Grebennikov discloses a wireless communication device comprising the amplifier circuit of claim 1 as mentioned in col. 1 lines 24-25, **meeting claim 13.**

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hieu Nguyen whose telephone number is 571-272-8577. The examiner can normally be reached on M-F 8-5.

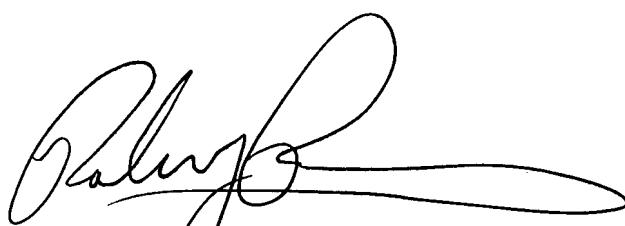
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hieu Nguyen
AU: 2817

Robert Pascal
Primary Examiner

hn



Robert Pascal
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Technology Center 2800